

<b>AMENDMENT TRANSMITTAL LETTER</b>			Docket No. TESSERA 3.0-320
Application No. 10/655,952-Conf. #8891	Filing Date September 5, 2003	Examiner S. B. Geyer	Art Unit 2815

Applicant(s): Teck-Gyu Kang and Yoichi Kubota

Invention: ASSEMBLIES HAVING STACKED SEMICONDUCTOR CHIPS AND METHODS OF MAKING SAME

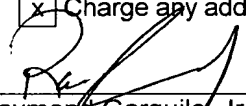
**TO THE COMMISSIONER FOR PATENTS**

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	32	- 40 =		x	
Independent Claims	5	- 7 =		x	
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
<b>TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:</b>					0.00

- ☒ Large Entity ☐ Small Entity
- ☒ No additional fee is required for this amendment.
- ☐ Please charge Deposit Account No. \_\_\_\_\_ in the amount of \$ \_\_\_\_\_.  
A duplicate copy of this sheet is enclosed.
- ☐ A check in the amount of \$ \_\_\_\_\_ to cover the filing fee is enclosed.
- ☐ Payment by credit card. Form PTO-2038 is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. 12-1095  
as described below. A duplicate copy of this sheet is enclosed.
- ☒ Credit any overpayment.
- ☒ Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.

  
Raymond Garguilo, Jr.  
Attorney Reg. No.: 50,930

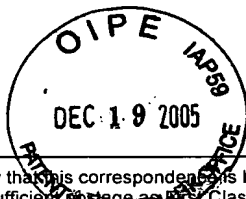
Dated: December 14, 2005

LENER, DAVID, LITTENBERG, KRUMHOLZ & MENTLIK, LLP  
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I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

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(Raymond Garguilo, Jr.)

Docket No.: TESSERA 3.0-320  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Kang et al.

Application No.: 10/655,952

Filed: September 5, 2003

For: ASSEMBLIES HAVING STACKED  
SEMICONDUCTOR CHIPS AND METHODS  
OF MAKING SAME

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: Group Art Unit: 2815  
:  
: Examiner: S. B. Geyer  
:  
:

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

In response to the official action mailed June 14, 2005, Applicant submits the following amendments and remarks.

A petition for a three-month extension of the shortened statutory time period for response to said official action up to and including December 14, 2005, is transmitted herewith.